



JL

Feb. 4, 2009

To: Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Fr: Stephen B. Ackerman, Reg. No. 37,761  
28 Davis Avenue  
Poughkeepsie, N.Y. 12603

Subject:

Serial No. 09/523,990

Mar. 13, 2000

MOU-SHIUNG LIN

"METHOD OF MANUFACTURE AND IDENTIFICATION OF  
SEMICONDUCTOR CHIP MARKED FOR IDENTIFICATION WITH  
INTERNAL MARKING INDICIA AND PROTECTION THEREOF BY  
NON-BLACK LAYER AND DEVICE PRODUCED THEREBY"

Grp. Art Unit: 2887

WALSH, DANIEL I

## RESPONSE TO NON-FINAL OFFICE ACTION

Dear Sir:

In response to the Non-Final Office Action mailed Nov. 4, 2008, please amend the  
above-identified application for patent and consider the remarks, as follows.

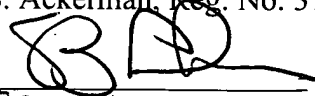
## CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal  
Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box  
1450, Alexandria, VA 22313-1450, on Feb. 4, 2009.

Stephen B. Ackerman, Reg. No. 37,761

Signature

Date

  
FEB. 4, 2009

Amendments to the Claims are reflected in the listing of the Claims which begins on page 3 of this paper.

Remarks/Arguments begin on page 7 of this paper.